

TEST

TEST 11 SUBTRACT
MD-11-DOK-A

EP-DOK-A-DL-A

NOV 1976

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FICHE 1 OF 1

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This microfiche card contains a grid of frames. The frames are arranged in approximately 10 rows and 4 columns. Each frame contains either text, a diagram, or a small table. The text is too small to read, but appears to be organized into sections. Some frames contain what looks like a flowchart or a diagram with lines connecting different points. The overall layout is a structured grid of information.

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IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DOAA TO DOLA-D
 PRODUCT NAME: FIRST LEVEL 1-12
 DATE CREATED: 17 MARCH 1970
 MAINTAINER: DIAGNOSTIC GROUP
 AUTHOR: JOHN HITTELL

THIS GROUP OF TESTS CONSIST OF:

PROCESSOR TEST	MAINDEC
TEST 1	BRANCH 11-DOAA
2	CON BRANCH 11-DOBA
3	UNARY 11-DOCA
4	UNARY AND BINARYS 11-DODA
5	ROTATE/SHIFT 11-DOEA
6	COMPARE 11-DOFA
7	COMPARE NOT 11-DOGA
8	MOVE 11-DOHA
9	BIS,BIC AND BIT 11-DOIA
10	ADD 11-DOJA
11	SUBTRACT 11-DOKA
12	JMP 11-DOLA

11-DOAA 11-DOBA 11-DOCA 11-DODA 11-DOEA 11-DOFA 11-DOGA 11-DOHA 11-DOIA 11-DOJA 11-DOKA 11-DOLA

1. ABSTRACT

THIS IS A GROUP OF 12 TESTS THAT INCREMENTALLY TEST AND ISOLATE SIMPLE MALFUNCTIONS OF THE PDP 11. THE TESTS SHOULD BE RUN IN THE INDICATED NUMERICAL SEQUENCE. THE SEQUENCE IS 1. BRANCH 2. CONDITIONAL BRANCH 3. UNARY 4. UNARY AND BINARYS 5. ROTATE/SHIFT 6. COMPARE (EQUALITY) 7. COMPARE (NON EQUALITY) 8. MOVE 9. BIT SET, CLEAR AND TEST 10. ADD 11. SUBTRACT 12. JUMP

THERE IS A SECOND GROUP OF TESTS FOR MORE COMPLEX MALFUNCTIONS.

2. REQUIREMENTS

2.1 EQUIPMENT

PDP-11/20 STANDARD COMPUTER

2.2 STORAGE

2.2.1 PROGRAM STORAGE - THE PROGRAMS USE ALL OF A 4KW MEMORY WITH THE EXCEPTION OF 17500 TO 17776 (WHICH IS RESERVED FOR THE BOOT AND ABSOLUTE LOADER).

3. LOADING PROCEDURE

3.1 METHOD

PROCEDURE FOR NORMAL ABSOLUTE TAPES SHOULD BE FOLLOWED.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTING

ALL PROGRAMS START AT ADDRESS 200.

4.2 STARTING ADDRESS OR ADDRESSES

(A) 200 = START AND RESTART ADDRESS

4.3 PROGRAM AND/OR OPERATOR ACTION

LOAD PROGRAM INTO MEMORY. (BOTTOM 4K)
SET SWITCH REGISTER TO STARTING ADDRESS. (200)
LOAD ADDRESS.
PRESS START.
THE PROGRAM WILL LOOP, AND RING A BELL AFTER A FIXED NUMBER OF ITERATIONS.

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- 5. OPERATING PROCEDURE
 - 5.1 OPERATIONAL SWITCH SETTINGS
 - 5.1.1 NO SWITCHES ARE USED
 - 5.1.3
 - 5.2 SUBROUTINE ABSTRACTS
 - 5.2.1 BEGIN SA 200
 - 5.2.2 SCOPE
 -
 - IS A "NOP" THAT IS PLACED BETWEEN EACH SUBTEST IN THE INSTRUCTION SECTION. IF A SCOPE LOOP IS NEEDED, INSERT A BRANCH TO THE PREVIOUS SCOPE LOCATION AT THE CURRENT SCOPE LOCATION. (NOTE NOP=240)
 - 5.2.3 HLT
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 - INDICATES THE UNIQUE ADDRESS THAT TAGS THE FAILING SUBTEST. THE INCORRECT DATA AT THE TIME OF THE FAILURE MAY OR MAY NOT BE DISPLAYED IN REGISTER ZERO, WHICH IS THE DATA REGISTER ON A HALT.
 - 5.3 PROGRAM AND/OR OPERATOR ACTION
 - 5.3.1 LOADING AND STARTING AT 200 STARTS THE TEST. IF AN ERROR IS DETECTED, THERE WILL BE A HALT. WHEN A HALT OCCURS AND IT IS NECESSARY TO SCOPE ON IT, PLACE INSERT A BRANCH INSTRUCTION IN THE SCOPE LOCATION FOLLOWING THE HALT. THE BRANCH INSTRUCTION SHOULD BRANCH YOU TO THE PREVIOUS SCOPE LOCATION.
- 6. ERRORS
 - 6.1 ALL ERRORS WILL CAUSE A HALT.
 - 6.2 ERROR RECOVERY
 - ON TRAP ERRORS - RESTART AT 200.
 - DEPRESS CONTINUE TO CONTINUE TEST
- 7. RESTRICTIONS
 - 7.1 STARTING RESTRICTION
 - NONE

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7.2 OPERATIONAL RESTRICTION
NONE

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8. MISCELLANEOUS

IF A HALT OCCURES IN THE TRAP OR INTERRUPT VECTOR AREA, REGISTER SIX SHOULD BE EXAMINED TO DETERMINE ITS CONTENTS, THEN USE REGISTER SIX CONTENTS AS AN ADDRESS TO DETERMINE THE LOCATION THE PROGRAM WAS AT, WHEN THE INTERRUPT OR TRAP OCCURRED. (MEMORY AS SPECIFIED BY R6 CONTAINS THE PC OF THE INSTRUCTION FOLLOWING THE INSTRUCTION WHERE THE TRAP OCCURED)

8.1 EXECUTION TIME

TIME BETWEEN BELLS

FOR TAPE 1	5	MINUTES
FOR TAPE 2	2.5	MINUTES
FOR TAPE 3	3	MINUTES
FOR TAPE 4	6	MINUTES
FOR TAPE 5	5	MINUTES
FOR TAPE 6	6	MINUTES
FOR TAPE 7	5	MINUTES
FOR TAPE 8	6	MINUTES
FOR TAPE 9	3	MINUTES
FOR TAPE 10	4	MINUTES
FOR TAPE 11	2	MINUTES
FOR TAPE 12	4	MINUTES

9. PROGRAM DESCRIPTION

- TEST 1 THIS IS A TEST OF THE UNCONDITIONAL BRANCH INSTRUCTION, IT PROVES THAT A LONG PROGRAM CAN BE LOADED AND THE INSTRUCTIONS CAN BE EXECUTED THROUGHOUT MEMORY.
- TEST 2 THIS IS A CONDITIONAL BRANCH TEST USING ALL OF THE CONDITIONAL BRANCH INSTRUCTIONS. IT TESTS THAT ALL COMBINATIONS OF CONDITION CODES ARE RESPONDED TO CORRECTLY. (THE CONDITION CODES ARE MANIPULATED WITH THE MICRO-PROGRAM OPERATIONS.)
- TEST 3 THIS IS A TEST OF THE UNARY INSTRUCTIONS AND THEIR EFFECTS UPON THE CONDITION CODES ONLY. MOSTLY REGISTER OPERATIONS.
- TEST 4 THIS IS A COMBINED USE OF UNARY AND BINARY INSTRUCTIONS TESTING THE RESULTS AND THE CONDITION CODES.
- TEST 5 THIS TEST ROTATE AND SHIFT INSTRUCTION, BOTH THE REGISTERS AND MEMORY LOCATIONS, WORD AND BYTE, ARE TESTED.
- TEST 6 THIS IS A TEST OF THE COMPARE INSTRUCTIONS FOR

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EQUALITY. ALL CONDITIONAL BRANCHES THAT WILL
BRANCH ON ANY FORM OF EQUALITY ARE ALSO TESTED.
BOTH WORD AND BYTE OPERATIONS ARE USED.


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252                                     %
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254                                     :TEST 11
255                                     :COPYRIGHT 1970, DIGITAL EQUIPMENT CORP., MAYNARD, MASS.
256                                     :TEST SUBTRACT INSTRUCTION PDP11
257                                     :=200
258                                     HLT=HALT
259                                     SCOPE=240
260
261                                     ;TEST SUBTRACT INSTRUCTIONS (D-S TO D)
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263                                     ;
264                                     SCOPE
265                                     MOV #0,%0 ;MOV 0 TO REGISTER ZERO
266                                     SUB #0,%0 ;SUB 0 FROM 0
267                                     CMP #0-0,%0 ;DOES REGISTER 0=0-0
268                                     BEQ .+4
269                                     HLT ;SUB 0 FROM 0 FAILED
270                                     SCOPE
271                                     MOV #177777,%0 ;MOV 177777 TO REGISTER ZERO
272                                     SUB #177777,%0 ;SUB 177777 FROM 177777
273                                     CMP #177777-177777,%0 ;DOES REGISTER 0=177777-177777
274                                     BEQ .+4
275                                     HLT ;SUB 177777 FROM 177777 FAILED
276                                     SCOPE
277                                     MOV #052525,%0 ;MOV 052525 TO REGISTER ZERO
278                                     SUB #125252,%0 ;SUB 125252 FROM 052525
279                                     CMP #052525-125252,%0 ;DOES REGISTER 0=125252-052525
280                                     BEQ .+4
281                                     HLT ;SUB 125252 FROM 052525 FAILED
282                                     SCOPE
283                                     MOV #125252,%0 ;MOV 125252 TO REGISTER ZERO
284                                     SUB #052525,%0 ;SUB 052525 FROM 125252
285                                     CMP #125252-052525,%0 ;DOES REGISTER 0=052525-125252
286                                     BEQ .+4
287                                     HLT ;SUB 052525 FROM 125252 FAILED
288                                     SCOPE
289                                     MOV #1,%0 ;MOV 1 TO REGISTER ZERO
290                                     SUB #0,%0 ;SUB 0 FROM 1
291                                     CMP #1-0,%0 ;DOES REGISTER 0=0-1
292                                     BEQ .+4
293                                     HLT ;SUB 0 FROM 1 FAILED
294
295                                     N=1
296                                     SCOPE
297                                     MOV #177777,%0 ;MOV 177777 TO REGISTER ZERO
298                                     SUB #1,%0 ;SUB 1 FROM 177777
299                                     CMP #177777-1,%0 ;DOES REGISTER 0=1-177777
300                                     BEQ .+4
301                                     HLT ;SUB 1 FROM 177777 FAILED
302                                     N=N+N
303                                     SCOPE
304                                     MOV #177777,%0 ;MOV 177777 TO REGISTER ZERO
305                                     SUB #2,%0 ;SUB 2 FROM 177777
306                                     CMP #177777-2,%0 ;DOES REGISTER 0=2-177777
307                                     BEQ .+4

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308	000374	000000		HLT		;SUB 2 FROM 177777 FAILED
309		000004		N=N+N		
310	000376	000240		SCOPE		
311	000400	012700	177777	MOV	#177777,%0	;MOV 177777 TO REGISTER ZERO
312	000404	162700	000004	SUB	#4,%0	;SUB 4 FROM 177777
313	000410	022700	177773	CMP	#177777-4,%0	;DOES REGISTER 0=4-177777
314	000414	001401		BEQ	+.4	
315	000416	000000		HLT		;SUB 4 FROM 177777 FAILED
316		000010		N=N+N		
317	000420	000240		SCOPE		
318	000422	012700	177777	MOV	#177777,%0	;MOV 177777 TO REGISTER ZERO
319	000426	162700	000010	SUB	#10,%0	;SUB 10 FROM 177777
320	000432	022700	177767	CMP	#177777-10,%0	;DOES REGISTER 0=10-177777
321	000436	001401		BEQ	+.4	
322	000440	000000		HLT		;SUB 10 FROM 177777 FAILED
323		000020		N=N+N		
324	000442	000240		SCOPE		
325	000444	012700	177777	MOV	#177777,%0	;MOV 177777 TO REGISTER ZERO
326	000450	162700	000020	SUB	#20,%0	;SUB 20 FROM 177777
327	000454	022700	177757	CMP	#177777-20,%0	;DOES REGISTER 0=20-177777
328	000460	001401		BEQ	+.4	
329	000462	000000		HLT		;SUB 20 FROM 177777 FAILED
330		000040		N=N+N		
331	000464	000240		SCOPE		
332	000466	012700	177777	MOV	#177777,%0	;MOV 177777 TO REGISTER ZERO
333	000472	162700	000040	SUB	#40,%0	;SUB 40 FROM 177777
334	000476	022700	177737	CMP	#177777-40,%0	;DOES REGISTER 0=40-177777
335	000502	001401		BEQ	+.4	
336	000504	000000		HLT		;SUB 40 FROM 177777 FAILED
337		000100		N=N+N		
338	000506	000240		SCOPE		
339	000510	012700	177777	MOV	#177777,%0	;MOV 177777 TO REGISTER ZERO
340	000514	162700	000100	SUB	#100,%0	;SUB 100 FROM 177777
341	000520	022700	177677	CMP	#177777-100,%0	;DOES REGISTER 0=100-177777
342	000524	001401		BEQ	+.4	
343	000526	000000		HLT		;SUB 100 FROM 177777 FAILED
344		000200		N=N+N		
345	000530	000240		SCOPE		
346	000532	012700	177777	MOV	#177777,%0	;MOV 177777 TO REGISTER ZERO
347	000536	162700	000200	SUB	#200,%0	;SUB 200 FROM 177777
348	000542	022700	177577	CMP	#177777-200,%0	;DOES REGISTER 0=200-177777
349	000546	001401		BEQ	+.4	
350	000550	000000		HLT		;SUB 200 FROM 177777 FAILED
351		000400		N=N+N		
352	000552	000240		SCOPE		
353	000554	012700	177777	MOV	#177777,%0	;MOV 177777 TO REGISTER ZERO
354	000560	162700	000400	SUB	#400,%0	;SUB 400 FROM 177777
355	000564	022700	177377	CMP	#177777-400,%0	;DOES REGISTER 0=400-177777
356	000570	001401		BEQ	+.4	
357	000572	000000		HLT		;SUB 400 FROM 177777 FAILED
358		001000		N=N+N		
359	000574	000240		SCOPE		
360	000576	012700	177777	MOV	#177777,%0	;MOV 177777 TO REGISTER ZERO
361	000602	162700	001000	SUB	#1000,%0	;SUB 1000 FROM 177777
362	000606	022700	176777	CMP	#177777-1000,%0	;DOES REGISTER 0=1000-177777
363	000612	001401		BEQ	+.4	

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364	000614	000000		HLT	;SUB 1000 FROM 177777 FAILED
365		002000		N=N+N	
366	000616	000240		SCOPE	
367	000620	012700	177777	MOV	#177777,%0 ;MOV 177777 TO REGISTER ZERO
368	000624	162700	002000	SUB	#2000,%0 ;SUB 2000 FROM 177777
369	000630	022700	175777	CMP	#177777-2000,%0 ;DOES REGISTER 0=2000-177777
370	000634	001401		BEQ	+.4
371	000636	000000		HLT	;SUB 2000 FROM 177777 FAILED
372		004000		N=N+N	
373	000640	000240		SCOPE	
374	000642	012700	177777	MOV	#177777,%0 ;MOV 177777 TO REGISTER ZERO
375	000646	162700	004000	SUB	#4000,%0 ;SUB 4000 FROM 177777
376	000652	022700	173777	CMP	#177777-4000,%0 ;DOES REGISTER 0=4000-177777
377	000656	001401		BEQ	+.4
378	000660	000000		HLT	;SUB 4000 FROM 177777 FAILED
379		010000		N=N+N	
380	000662	000240		SCOPE	
381	000664	012700	177777	MOV	#177777,%0 ;MOV 177777 TO REGISTER ZERO
382	000670	162700	010000	SUB	#10000,%0 ;SUB 10000 FROM 177777
383	000674	022700	167777	CMP	#177777-10000,%0 ;DOES REGISTER 0=10000-177777
384	000700	001401		BEQ	+.4
385	000702	000000		HLT	;SUB 10000 FROM 177777 FAILED
386		020000		N=N+N	
387	000704	000240		SCOPE	
388	000706	012700	177777	MOV	#177777,%0 ;MOV 177777 TO REGISTER ZERO
389	000712	162700	020000	SUB	#20000,%0 ;SUB 20000 FROM 177777
390	000716	022700	157777	CMP	#177777-20000,%0 ;DOES REGISTER 0=20000-177777
391	000722	001401		BEQ	+.4
392	000724	000000		HLT	;SUB 20000 FROM 177777 FAILED
393		040000		N=N+N	
394	000726	000240		SCOPE	
395	000730	012700	177777	MOV	#177777,%0 ;MOV 177777 TO REGISTER ZERO
396	000734	162700	040000	SUB	#40000,%0 ;SUB 40000 FROM 177777
397	000740	022700	137777	CMP	#177777-40000,%0 ;DOES REGISTER 0=40000-177777
398	000744	001401		BEQ	+.4
399	000746	000000		HLT	;SUB 40000 FROM 177777 FAILED
400		100000		N=N+N	
401	000750	000240		SCOPE	
402	000752	012700	177777	MOV	#177777,%0 ;MOV 177777 TO REGISTER ZERO
403	000756	162700	100000	SUB	#100000,%0 ;SUB 100000 FROM 177777
404	000762	022700	077777	CMP	#177777-100000,%0 ;DOES REGISTER 0=100000-177777
405	000766	001401		BEQ	+.4
406	000770	000000		HLT	;SUB 100000 FROM 177777 FAILED
407		000000		N=N+N	
408					
409		000001		N=1	

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410	000772	000240		SCOPE	
411	000774	012700	000001	MOV	#1,%0 ;MOV 1 TO REGISTER ZERO
412	001000	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 1
413	001004	022700	000002	CMP	#1-177777,%0 ;DOES REGISTER 0=177777-1
414	001010	001401		BEQ	+.4
415	001012	000000		HLT	;SUB 177777 FROM 1 FAILED
416		000002		N=N+N	
417	001014	000240		SCOPE	
418	001016	012700	000002	MOV	#2,%0 ;MOV 2 TO REGISTER ZERO
419	001022	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 2
420	001026	022700	000003	CMP	#2-177777,%0 ;DOES REGISTER 0=177777-2
421	001032	001401		BEQ	+.4
422	001034	000000		HLT	;SUB 177777 FROM 2 FAILED
423		000004		N=N+N	
424	001036	000240		SCOPE	
425	001040	012700	000004	MOV	#4,%0 ;MOV 4 TO REGISTER ZERO
426	001044	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 4
427	001050	022700	000005	CMP	#4-177777,%0 ;DOES REGISTER 0=177777-4
428	001054	001401		BEQ	+.4
429	001056	000000		HLT	;SUB 177777 FROM 4 FAILED
430		000010		N=N+N	
431	001060	000240		SCOPE	
432	001062	012700	000010	MOV	#10,%0 ;MOV 10 TO REGISTER ZERO
433	001066	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 10
434	001072	022700	000011	CMP	#10-177777,%0 ;DOES REGISTER 0=177777-10
435	001076	001401		BEQ	+.4
436	001100	000000		HLT	;SUB 177777 FROM 10 FAILED
437		000020		N=N+N	
438	001102	000240		SCOPE	
439	001104	012700	000020	MOV	#20,%0 ;MOV 20 TO REGISTER ZERO
440	001110	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 20
441	001114	022700	000021	CMP	#20-177777,%0 ;DOES REGISTER 0=177777-20
442	001120	001401		BEQ	+.4
443	001122	000000		HLT	;SUB 177777 FROM 20 FAILED
444		000040		N=N+N	
445	001124	000240		SCOPE	
446	001126	012700	000040	MOV	#40,%0 ;MOV 40 TO REGISTER ZERO
447	001132	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 40
448	001136	022700	000041	CMP	#40-177777,%0 ;DOES REGISTER 0=177777-40
449	001142	001401		BEQ	+.4
450	001144	000000		HLT	;SUB 177777 FROM 40 FAILED
451		000100		N=N+N	
452	001146	000240		SCOPE	
453	001150	012700	000100	MOV	#100,%0 ;MOV 100 TO REGISTER ZERO
454	001154	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 100
455	001160	022700	000101	CMP	#100-177777,%0 ;DOES REGISTER 0=177777-100
456	001164	001401		BEQ	+.4
457	001166	000000		HLT	;SUB 177777 FROM 100 FAILED
458		000200		N=N+N	
459	001170	000240		SCOPE	
460	001172	012700	000200	MOV	#200,%0 ;MOV 200 TO REGISTER ZERO
461	001176	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 200
462	001202	022700	000201	CMP	#200-177777,%0 ;DOES REGISTER 0=177777-200
463	001206	001401		BEQ	+.4
464	001210	000000		HLT	;SUB 177777 FROM 200 FAILED
465		000400		N=N+N	

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466	001212	000240		SCOPE	
467	001214	012700	000400	MOV	#400,%0 ;MOV 400 TO REGISTER ZERO
468	001220	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 400
469	001224	022700	000401	CMP	#400-177777,%0 ;DOES REGISTER 0=177777-400
470	001230	001401		BEQ	.+4
471	001232	000000		HLT	;SUB 177777 FROM 400 FAILED
472		001000		N=N+N	
473	001234	000240		SCOPE	
474	001236	012700	001000	MOV	#1000,%0 ;MOV 1000 TO REGISTER ZERO
475	001242	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 1000
476	001246	022700	001001	CMP	#1000-177777,%0 ;DOES REGISTER 0=177777-1000
477	001252	001401		BEQ	.+4
478	001254	000000		HLT	;SUB 177777 FROM 1000 FAILED
479		002000		N=N+N	
480	001256	000240		SCOPE	
481	001260	012700	002000	MOV	#2000,%0 ;MOV 2000 TO REGISTER ZERO
482	001264	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 2000
483	001270	022700	002001	CMP	#2000-177777,%0 ;DOES REGISTER 0=177777-2000
484	001274	001401		BEQ	.+4
485	001276	000000		HLT	;SUB 177777 FROM 2000 FAILED
486		004000		N=N+N	
487	001300	000240		SCOPE	
488	001302	012700	004000	MOV	#4000,%0 ;MOV 4000 TO REGISTER ZERO
489	001306	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 4000
490	001312	022700	004001	CMP	#4000-177777,%0 ;DOES REGISTER 0=177777-4000
491	001316	001401		BEQ	.+4
492	001320	000000		HLT	;SUB 177777 FROM 4000 FAILED
493		010000		N=N+N	
494	001322	000240		SCOPE	
495	001324	012700	010000	MOV	#10000,%0 ;MOV 10000 TO REGISTER ZERO
496	001330	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 10000
497	001334	022700	010001	CMP	#10000-177777,%0 ;DOES REGISTER 0=177777-10000
498	001340	001401		BEQ	.+4
499	001342	000000		HLT	;SUB 177777 FROM 10000 FAILED
500		020000		N=N+N	
501	001344	000240		SCOPE	
502	001346	012700	020000	MOV	#20000,%0 ;MOV 20000 TO REGISTER ZERO
503	001352	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 20000
504	001356	022700	020001	CMP	#20000-177777,%0 ;DOES REGISTER 0=177777-20000
505	001362	001401		BEQ	.+4
506	001364	000000		HLT	;SUB 177777 FROM 20000 FAILED
507		040000		N=N+N	
508	001366	000240		SCOPE	
509	001370	012700	040000	MOV	#40000,%0 ;MOV 40000 TO REGISTER ZERO
510	001374	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 40000
511	001400	022700	040001	CMP	#40000-177777,%0 ;DOES REGISTER 0=177777-40000
512	001404	001401		BEQ	.+4
513	001406	000000		HLT	;SUB 177777 FROM 40000 FAILED
514		100000		N=N+N	
515	001410	000240		SCOPE	
516	001412	012700	100000	MOV	#100000,%0 ;MOV 100000 TO REGISTER ZERO
517	001416	162700	177777	SUB	#177777,%0 ;SUB 177777 FROM 100000
518	001422	022700	100001	CMP	#100000-177777,%0 ;DOES REGISTER 0=177777-100000
519	001426	001401		BEQ	.+4
520	001430	000000		HLT	;SUB 177777 FROM 100000 FAILED
521		000000		N=N+N	

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522					
523		000001		N=1	
524	001432	000240		SCOPE	
525	001434	012700	000001	MOV	#1,%0 ;MOV 1 TO REGISTER ZERO
526	001440	162700	000000	SUB	#0,%0 ;SUB 0 FROM 1
527	001444	022700	000001	CMP	#1-0,%0 ;DOES REGISTER 0=0-1
528	001450	001401		BEQ	.+4
529	001452	000000		HLT	;SUB 0 FROM 1 FAILED
530		000002		N=N+N	
531	001454	000240		SCOPE	
532	001456	012700	000002	MOV	#2,%0 ;MOV 2 TO REGISTER ZERO
533	001462	162700	000000	SUB	#0,%0 ;SUB 0 FROM 2
534	001466	022700	000002	CMP	#2-0,%0 ;DOES REGISTER 0=0-2
535	001472	001401		BEQ	.+4
536	001474	000000		HLT	;SUB 0 FROM 2 FAILED
537		000004		N=N+N	
538	001476	000240		SCOPE	
539	001500	012700	000004	MOV	#4,%0 ;MOV 4 TO REGISTER ZERO
540	001504	162700	000000	SUB	#0,%0 ;SUB 0 FROM 4
541	001510	022700	000004	CMP	#4-0,%0 ;DOES REGISTER 0=0-4
542	001514	001401		BEQ	.+4
543	001516	000000		HLT	;SUB 0 FROM 4 FAILED
544		000010		N=N+N	
545	001520	000240		SCOPE	
546	001522	012700	000010	MOV	#10,%0 ;MOV 10 TO REGISTER ZERO
547	001526	162700	000000	SUB	#0,%0 ;SUB 0 FROM 10
548	001532	022700	000010	CMP	#10-0,%0 ;DOES REGISTER 0=0-10
549	001536	001401		BEQ	.+4
550	001540	000000		HLT	;SUB 0 FROM 10 FAILED
551		000020		N=N+N	
552	001542	000240		SCOPE	
553	001544	012700	000020	MOV	#20,%0 ;MOV 20 TO REGISTER ZERO
554	001550	162700	000000	SUB	#0,%0 ;SUB 0 FROM 20
555	001554	022700	000020	CMP	#20-0,%0 ;DOES REGISTER 0=0-20
556	001560	001401		BEQ	.+4
557	001562	000000		HLT	;SUB 0 FROM 20 FAILED
558		000040		N=N+N	
559	001564	000240		SCOPE	
560	001566	012700	000040	MOV	#40,%0 ;MOV 40 TO REGISTER ZERO
561	001572	162700	000000	SUB	#0,%0 ;SUB 0 FROM 40
562	001576	022700	000040	CMP	#40-0,%0 ;DOES REGISTER 0=0-40
563	001602	001401		BEQ	.+4
564	001604	000000		HLT	;SUB 0 FROM 40 FAILED
565		000100		N=N+N	
566	001606	000240		SCOPE	
567	001610	012700	000100	MOV	#100,%0 ;MOV 100 TO REGISTER ZERO
568	001614	162700	000000	SUB	#0,%0 ;SUB 0 FROM 100
569	001620	022700	000100	CMP	#100-0,%0 ;DOES REGISTER 0=0-100
570	001624	001401		BEQ	.+4
571	001626	000000		HLT	;SUB 0 FROM 100 FAILED
572		000200		N=N+N	
573	001630	000240		SCOPE	
574	001632	012700	000200	MOV	#200,%0 ;MOV 200 TO REGISTER ZERO
575	001636	162700	000000	SUB	#0,%0 ;SUB 0 FROM 200
576	001642	022700	000200	CMP	#200-0,%0 ;DOES REGISTER 0=0-200
577	001646	001401		BEQ	.+4

578	001650	000000		HLT		;SUB 0 FROM 200 FAILED
579		000400		N=N+N		
580	001652	000240		SCOPE		
581	001654	012700	000400	MOV	#400,%0	;MOV 400 TO REGISTER ZERO
582	001660	162700	000000	SUB	#0,%0	;SUB 0 FROM 400
583	001664	022700	000400	CMP	#400-0,%0	;DOES REGISTER 0=0-400
584	001670	001401		BEQ	.+4	
585	001672	000000		HLT		;SUB 0 FROM 400 FAILED
586		001000		N=N+N		
587	001674	000240		SCOPE		
588	001676	012700	001000	MOV	#1000,%0	;MOV 1000 TO REGISTER ZERO
589	001702	162700	000000	SUB	#0,%0	;SUB 0 FROM 1000
590	001706	022700	001000	CMP	#1000-0,%0	;DOES REGISTER 0=0-1000
591	001712	001401		BEQ	.+4	
592	001714	000000		HLT		;SUB 0 FROM 1000 FAILED
593		002000		N=N+N		
594	001716	000240		SCOPE		
595	001720	012700	002000	MOV	#2000,%0	;MOV 2000 TO REGISTER ZERO
596	001724	162700	000000	SUB	#0,%0	;SUB 0 FROM 2000
597	001730	022700	002000	CMP	#2000-0,%0	;DOES REGISTER 0=0-2000
598	001734	001401		BEQ	.+4	
599	001736	000000		HLT		;SUB 0 FROM 2000 FAILED
600		004000		N=N+N		
601	001740	000240		SCOPE		
602	001742	012700	004000	MOV	#4000,%0	;MOV 4000 TO REGISTER ZERO
603	001746	162700	000000	SUB	#0,%0	;SUB 0 FROM 4000
604	001752	022700	004000	CMP	#4000-0,%0	;DOES REGISTER 0=0-4000
605	001756	001401		BEQ	.+4	
606	001760	000000		HLT		;SUB 0 FROM 4000 FAILED
607		010000		N=N+N		
608	001762	000240		SCOPE		
609	001764	012700	010000	MOV	#10000,%0	;MOV 10000 TO REGISTER ZERO
610	001770	162700	000000	SUB	#0,%0	;SUB 0 FROM 10000
611	001774	022700	010000	CMP	#10000-0,%0	;DOES REGISTER 0=0-10000
612	002000	001401		BEQ	.+4	
613	002002	000000		HLT		;SUB 0 FROM 10000 FAILED
614		020000		N=N+N		
615	002004	000240		SCOPE		
616	002006	012700	020000	MOV	#20000,%0	;MOV 20000 TO REGISTER ZERO
617	002012	162700	000000	SUB	#0,%0	;SUB 0 FROM 20000
618	002016	022700	020000	CMP	#20000-0,%0	;DOES REGISTER 0=0-20000
619	002022	001401		BEQ	.+4	
620	002024	000000		HLT		;SUB 0 FROM 20000 FAILED
621		040000		N=N+N		
622	002026	000240		SCOPE		
623	002030	012700	040000	MOV	#40000,%0	;MOV 40000 TO REGISTER ZERO
624	002034	162700	000000	SUB	#0,%0	;SUB 0 FROM 40000
625	002040	022700	040000	CMP	#40000-0,%0	;DOES REGISTER 0=0-40000
626	002044	001401		BEQ	.+4	
627	002046	000000		HLT		;SUB 0 FROM 40000 FAILED
628		100000		N=N+N		
629	002050	000240		SCOPE		
630	002052	012700	100000	MOV	#100000,%0	;MOV 100000 TO REGISTER ZERO
631	002056	162700	000000	SUB	#0,%0	;SUB 0 FROM 100000
632	002062	022700	100000	CMP	#100000-0,%0	;DOES REGISTER 0=0-100000
633	002066	001401		BEQ	.+4	

634	002070	000000		HLT		;SUB 0 FROM 100000 FAILED
635		000000		N=N+N		
636						
637		000001		N=1		
638	002072	000240		SCOPE		
639	002074	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
640	002100	162700	000001	SUB	#1,%0	;SUB 1 FROM 0
641	002104	022700	177777	CMP	#0-1,%0	;DOES REGISTER 0=1-0
642	002110	001401		BEQ	.+4	
643	002112	000000		HLT		;SUB 1 FROM 0 FAILED
644		000002		N=N+N		
645	002114	000240		SCOPE		
646	002116	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
647	002122	162700	000002	SUB	#2,%0	;SUB 2 FROM 0
648	002126	022700	177776	CMP	#0-2,%0	;DOES REGISTER 0=2-0
649	002132	001401		BEQ	.+4	
650	002134	000000		HLT		;SUB 2 FROM 0 FAILED
651		000004		N=N+N		
652	002136	000240		SCOPE		
653	002140	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
654	002144	162700	000004	SUB	#4,%0	;SUB 4 FROM 0
655	002150	022700	177774	CMP	#0-4,%0	;DOES REGISTER 0=4-0
656	002154	001401		BEQ	.+4	
657	002156	000000		HLT		;SUB 4 FROM 0 FAILED
658		000010		N=N+N		
659	002160	000240		SCOPE		
660	002162	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
661	002166	162700	000010	SUB	#10,%0	;SUB 10 FROM 0
662	002172	022700	177770	CMP	#0-10,%0	;DOES REGISTER 0=10-0
663	002176	001401		BEQ	.+4	
664	002200	000000		HLT		;SUB 10 FROM 0 FAILED
665		000020		N=N+N		
666	002202	000240		SCOPE		
667	002204	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
668	002210	162700	000020	SUB	#20,%0	;SUB 20 FROM 0
669	002214	022700	177760	CMP	#0-20,%0	;DOES REGISTER 0=20-0
670	002220	001401		BEQ	.+4	
671	002222	000000		HLT		;SUB 20 FROM 0 FAILED
672		000040		N=N+N		
673	002224	000240		SCOPE		
674	002226	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
675	002232	162700	000040	SUB	#40,%0	;SUB 40 FROM 0
676	002236	022700	177740	CMP	#0-40,%0	;DOES REGISTER 0=40-0
677	002242	001401		BEQ	.+4	
678	002244	000000		HLT		;SUB 40 FROM 0 FAILED
679		000100		N=N+N		
680	002246	000240		SCOPE		
681	002250	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
682	002254	162700	000100	SUB	#100,%0	;SUB 100 FROM 0
683	002260	022700	177700	CMP	#0-100,%0	;DOES REGISTER 0=100-0
684	002264	001401		BEQ	.+4	
685	002266	000000		HLT		;SUB 100 FROM 0 FAILED
686		000200		N=N+N		
687	002270	000240		SCOPE		
688	002272	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
689	002276	162700	000200	SUB	#200,%0	;SUB 200 FROM 0

690	002302	022700	177600	CMP	#0-200,%0	;DOES REGISTER 0=200-0
691	002306	001401		BEQ	.+4	
692	002310	000000		HLT		;SUB 200 FROM 0 FAILED
693		000400		N=N+N		
694	002312	000240		SCOPE		
695	002314	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
696	002320	162700	000400	SUB	#400,%0	;SUB 400 FROM 0
697	002324	022700	177400	CMP	#0-400,%0	;DOES REGISTER 0=400-0
698	002330	001401		BEQ	.+4	
699	002332	000000		HLT		;SUB 400 FROM 0 FAILED
700		001000		N=N+N		
701	002334	000240		SCOPE		
702	002336	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
703	002342	162700	001000	SUB	#1000,%0	;SUB 1000 FROM 0
704	002346	022700	177000	CMP	#0-1000,%0	;DOES REGISTER 0=1000-0
705	002352	001401		BEQ	.+4	
706	002354	000000		HLT		;SUB 1000 FROM 0 FAILED
707		002000		N=N+N		
708	002356	000240		SCOPE		
709	002360	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
710	002364	162700	002000	SUB	#2000,%0	;SUB 2000 FROM 0
711	002370	022700	176000	CMP	#0-2000,%0	;DOES REGISTER 0=2000-0
712	002374	001401		BEQ	.+4	
713	002376	000000		HLT		;SUB 2000 FROM 0 FAILED
714		004000		N=N+N		
715	002400	000240		SCOPE		
716	002402	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
717	002406	162700	004000	SUB	#4000,%0	;SUB 4000 FROM 0
718	002412	022700	174000	CMP	#0-4000,%0	;DOES REGISTER 0=4000-0
719	002416	001401		BEQ	.+4	
720	002420	000000		HLT		;SUB 4000 FROM 0 FAILED
721		010000		N=N+N		
722	002422	000240		SCOPE		
723	002424	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
724	002430	162700	010000	SUB	#10000,%0	;SUB 10000 FROM 0
725	002434	022700	170000	CMP	#0-10000,%0	;DOES REGISTER 0=10000-0
726	002440	001401		BEQ	.+4	
727	002442	000000		HLT		;SUB 10000 FROM 0 FAILED
728		020000		N=N+N		
729	002444	000240		SCOPE		
730	002446	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
731	002452	162700	020000	SUB	#20000,%0	;SUB 20000 FROM 0
732	002456	022700	160000	CMP	#0-20000,%0	;DOES REGISTER 0=20000-0
733	002462	001401		BEQ	.+4	
734	002464	000000		HLT		;SUB 20000 FROM 0 FAILED
735		040000		N=N+N		
736	002466	000240		SCOPE		
737	002470	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
738	002474	162700	040000	SUB	#40000,%0	;SUB 40000 FROM 0
739	002500	022700	140000	CMP	#0-40000,%0	;DOES REGISTER 0=40000-0
740	002504	001401		BEQ	.+4	
741	002506	000000		HLT		;SUB 40000 FROM 0 FAILED
742		100000		N=N+N		
743	002510	000240		SCOPE		
744	002512	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
745	002516	162700	100000	SUB	#100000,%0	;SUB 100000 FROM 0

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746	002522	022700	100000	CMP	#0-100000,%0	;DOES REGISTER 0=100000-0
747	002526	001401		BEQ	+.4	
748	002530	000000		HLT		;SUB 100000 FROM 0 FAILED
749		000000		N=N+N		
750						
751		000000		M=0		
752		000000		N=0		
753	002532	000240		SCOPE		
754	002534	012700	000000	MOV	#0,%0	;MOV 0 TO REGISTER ZERO
755	002540	162700	000000	SUB	#0,%0	;SUB 0 FROM 0
756	002544	022700	000000	CMP	#0-0,%0	;DOES REGISTER 0=0-0
757	002550	001401		BEQ	+.4	
758	002552	000000		HLT		;SUB 0 FROM 0 FAILED
759		000001		N=N+1&17525+N		
760		000401		M=M+401		
761	002554	000240		SCOPE		
762	002556	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
763	002562	162700	000401	SUB	#401,%0	;SUB 401 FROM 1
764	002566	022700	177400	CMP	#1-401,%0	;DOES REGISTER 0=401-1
765	002572	001401		BEQ	+.4	
766	002574	000000		HLT		;SUB 401 FROM 1 FAILED
767		000001		N=N+1&17525+N		
768		001002		M=M+401		
769	002576	000240		SCOPE		
770	002600	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
771	002604	162700	001002	SUB	#1002,%0	;SUB 1002 FROM 1
772	002610	022700	176777	CMP	#1-1002,%0	;DOES REGISTER 0=1002-1
773	002614	001401		BEQ	+.4	
774	002616	000000		HLT		;SUB 1002 FROM 1 FAILED
775		000001		N=N+1&17525+N		
776		001403		M=M+401		
777	002620	000240		SCOPE		
778	002622	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
779	002626	162700	001403	SUB	#1403,%0	;SUB 1403 FROM 1
780	002632	022700	176376	CMP	#1-1403,%0	;DOES REGISTER 0=1403-1
781	002636	001401		BEQ	+.4	
782	002640	000000		HLT		;SUB 1403 FROM 1 FAILED
783		000001		N=N+1&17525+N		
784		002004		M=M+401		
785	002642	000240		SCOPE		
786	002644	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
787	002650	162700	002004	SUB	#2004,%0	;SUB 2004 FROM 1
788	002654	022700	175775	CMP	#1-2004,%0	;DOES REGISTER 0=2004-1
789	002660	001401		BEQ	+.4	
790	002662	000000		HLT		;SUB 2004 FROM 1 FAILED
791		000001		N=N+1&17525+N		
792		002405		M=M+401		
793	002664	000240		SCOPE		
794	002666	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
795	002672	162700	002405	SUB	#2405,%0	;SUB 2405 FROM 1
796	002676	022700	175374	CMP	#1-2405,%0	;DOES REGISTER 0=2405-1
797	002702	001401		BEQ	+.4	
798	002704	000000		HLT		;SUB 2405 FROM 1 FAILED
799		000001		N=N+1&17525+N		
800		003006		M=M+401		
801	002706	000240		SCOPE		

802	002710	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
803	002714	162700	003006	SUB	#3006,%0	;SUB 3006 FROM 1
804	002720	022700	174773	CMP	#1-3006,%0	;DOES REGISTER 0=3006-1
805	002724	001401		BEQ	.+4	
806	002726	000000		HLT		;SUB 3006 FROM 1 FAILED
807		000001		N=N+1&17525+N		
808		003407		M=M+401		
809	002730	000240		SCOPE		
810	002732	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
811	002736	162700	003407	SUB	#3407,%0	;SUB 3407 FROM 1
812	002742	022700	174372	CMP	#1-3407,%0	;DOES REGISTER 0=3407-1
813	002746	001401		BEQ	.+4	
814	002750	000000		HLT		;SUB 3407 FROM 1 FAILED
815		000001		N=N+1&17525+N		
816		004010		M=M+401		
817	002752	000240		SCOPE		
818	002754	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
819	002760	162700	004010	SUB	#4010,%0	;SUB 4010 FROM 1
820	002764	022700	173771	CMP	#1-4010,%0	;DOES REGISTER 0=4010-1
821	002770	001401		BEQ	.+4	
822	002772	000000		HLT		;SUB 4010 FROM 1 FAILED
823		000001		N=N+1&17525+N		
824		004411		M=M+401		
825	002774	000240		SCOPE		
826	002776	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
827	003002	162700	004411	SUB	#4411,%0	;SUB 4411 FROM 1
828	003006	022700	173370	CMP	#1-4411,%0	;DOES REGISTER 0=4411-1
829	003012	001401		BEQ	.+4	
830	003014	000000		HLT		;SUB 4411 FROM 1 FAILED
831		000001		N=N+1&17525+N		
832		005012		M=M+401		
833	003016	000240		SCOPE		
834	003020	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
835	003024	162700	005012	SUB	#5012,%0	;SUB 5012 FROM 1
836	003030	022700	172767	CMP	#1-5012,%0	;DOES REGISTER 0=5012-1
837	003034	001401		BEQ	.+4	
838	003036	000000		HLT		;SUB 5012 FROM 1 FAILED
839		000001		N=N+1&17525+N		
840		005413		M=M+401		
841	003040	000240		SCOPE		
842	003042	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
843	003046	162700	005413	SUB	#5413,%0	;SUB 5413 FROM 1
844	003052	022700	172366	CMP	#1-5413,%0	;DOES REGISTER 0=5413-1
845	003056	001401		BEQ	.+4	
846	003060	000000		HLT		;SUB 5413 FROM 1 FAILED
847		000001		N=N+1&17525+N		
848		006014		M=M+401		
849	003062	000240		SCOPE		
850	003064	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
851	003070	162700	006014	SUB	#6014,%0	;SUB 6014 FROM 1
852	003074	022700	171765	CMP	#1-6014,%0	;DOES REGISTER 0=6014-1
853	003100	001401		BEQ	.+4	
854	003102	000000		HLT		;SUB 6014 FROM 1 FAILED
855		000001		N=N+1&17525+N		
856		006415		M=M+401		
857	003104	000240		SCOPE		

858	003106	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
859	003112	162700	006415	SUB	#6415,%0	;SUB 6415 FROM 1
860	003116	022700	171364	CMP	#1-6415,%0	;DOES REGISTER 0=6415-1
861	003122	001401		BEQ	.+4	
862	003124	000000		HLT		;SUB 6415 FROM 1 FAILED
863		000001		N=N+1&17525+N		
864		007016		M=M+401		
865	003126	000240		SCOPE		
866	003130	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
867	003134	162700	007016	SUB	#7016,%0	;SUB 7016 FROM 1
868	003140	022700	170763	CMP	#1-7016,%0	;DOES REGISTER 0=7016-1
869	003144	001401		BEQ	.+4	
870	003146	000000		HLT		;SUB 7016 FROM 1 FAILED
871		000001		N=N+1&17525+N		
872		007417		M=M+401		
873	003150	000240		SCOPE		
874	003152	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
875	003156	162700	007417	SUB	#7417,%0	;SUB 7417 FROM 1
876	003162	022700	170362	CMP	#1-7417,%0	;DOES REGISTER 0=7417-1
877	003166	001401		BEQ	.+4	
878	003170	000000		HLT		;SUB 7417 FROM 1 FAILED
879		000001		N=N+1&17525+N		
880		010020		M=M+401		
881	003172	000240		SCOPE		
882	003174	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
883	003200	162700	010020	SUB	#10020,%0	;SUB 10020 FROM 1
884	003204	022700	167761	CMP	#1-10020,%0	;DOES REGISTER 0=10020-1
885	003210	001401		BEQ	.+4	
886	003212	000000		HLT		;SUB 10020 FROM 1 FAILED
887		000001		N=N+1&17525+N		
888		010421		M=M+401		
889	003214	000240		SCOPE		
890	003216	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
891	003222	162700	010421	SUB	#10421,%0	;SUB 10421 FROM 1
892	003226	022700	167360	CMP	#1-10421,%0	;DOES REGISTER 0=10421-1
893	003232	001401		BEQ	.+4	
894	003234	000000		HLT		;SUB 10421 FROM 1 FAILED
895		000001		N=N+1&17525+N		
896		011022		M=M+401		
897	003236	000240		SCOPE		
898	003240	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
899	003244	162700	011022	SUB	#11022,%0	;SUB 11022 FROM 1
900	003250	022700	166757	CMP	#1-11022,%0	;DOES REGISTER 0=11022-1
901	003254	001401		BEQ	.+4	
902	003256	000000		HLT		;SUB 11022 FROM 1 FAILED
903		000001		N=N+1&17525+N		
904		011423		M=M+401		
905	003260	000240		SCOPE		
906	003262	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
907	003266	162700	011423	SUB	#11423,%0	;SUB 11423 FROM 1
908	003272	022700	166356	CMP	#1-11423,%0	;DOES REGISTER 0=11423-1
909	003276	001401		BEQ	.+4	
910	003300	000000		HLT		;SUB 11423 FROM 1 FAILED
911		000001		N=N+1&17525+N		
912		012024		M=M+401		
913	003302	000240		SCOPE		

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914	003304	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
915	003310	162700	012024	SUB	#12024,%0	;SUB 12024 FROM 1
916	003314	022700	165755	CMP	#1-12024,%0	;DOES REGISTER 0=12024-1
917	003320	001401		BEQ	.+4	
918	003322	000000		HLT		;SUB 12024 FROM 1 FAILED
919		000001		N=N+1&17525+N		
920		012425		M=M+401		
921	003324	000240		SCOPE		
922	003326	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
923	003332	162700	012425	SUB	#12425,%0	;SUB 12425 FROM 1
924	003336	022700	165354	CMP	#1-12425,%0	;DOES REGISTER 0=12425-1
925	003342	001401		BEQ	.+4	
926	003344	000000		HLT		;SUB 12425 FROM 1 FAILED
927		000001		N=N+1&17525+N		
928		013026		M=M+401		
929	003346	000240		SCOPE		
930	003350	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
931	003354	162700	013026	SUB	#13026,%0	;SUB 13026 FROM 1
932	003360	022700	164753	CMP	#1-13026,%0	;DOES REGISTER 0=13026-1
933	003364	001401		BEQ	.+4	
934	003366	000000		HLT		;SUB 13026 FROM 1 FAILED
935		000001		N=N+1&17525+N		
936		013427		M=M+401		
937	003370	000240		SCOPE		
938	003372	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
939	003376	162700	013427	SUB	#13427,%0	;SUB 13427 FROM 1
940	003402	022700	164352	CMP	#1-13427,%0	;DOES REGISTER 0=13427-1
941	003406	001401		BEQ	.+4	
942	003410	000000		HLT		;SUB 13427 FROM 1 FAILED
943		000001		N=N+1&17525+N		
944		014030		M=M+401		
945	003412	000240		SCOPE		
946	003414	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
947	003420	162700	014030	SUB	#14030,%0	;SUB 14030 FROM 1
948	003424	022700	163751	CMP	#1-14030,%0	;DOES REGISTER 0=14030-1
949	003430	001401		BEQ	.+4	
950	003432	000000		HLT		;SUB 14030 FROM 1 FAILED
951		000001		N=N+1&17525+N		
952		014431		M=M+401		
953	003434	000240		SCOPE		
954	003436	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
955	003442	162700	014431	SUB	#14431,%0	;SUB 14431 FROM 1
956	003446	022700	163350	CMP	#1-14431,%0	;DOES REGISTER 0=14431-1
957	003452	001401		BEQ	.+4	
958	003454	000000		HLT		;SUB 14431 FROM 1 FAILED
959		000001		N=N+1&17525+N		
960		015032		M=M+401		
961	003456	000240		SCOPE		
962	003460	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
963	003464	162700	015032	SUB	#15032,%0	;SUB 15032 FROM 1
964	003470	022700	162747	CMP	#1-15032,%0	;DOES REGISTER 0=15032-1
965	003474	001401		BEQ	.+4	
966	003476	000000		HLT		;SUB 15032 FROM 1 FAILED
967		000001		N=N+1&17525+N		
968		015433		M=M+401		
969	003500	000240		SCOPE		

970	003502	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
971	003506	162700	015433	SUB	#15433,%0	:SUB 15433 FROM 1
972	003512	022700	162346	CMP	#1-15433,%0	:DOES REGISTER 0=15433-1
973	003516	001401		BEQ	.+4	
974	003520	000000		HLT		:SUB 15433 FROM 1 FAILED
975		000001		N=N+1&17525+N		
976		016034		M=M+401		
977	003522	000240		SCOPE		
978	003524	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
979	003530	162700	016034	SUB	#16034,%0	:SUB 16034 FROM 1
980	003534	022700	161745	CMP	#1-16034,%0	:DOES REGISTER 0=16034-1
981	003540	001401		BEQ	.+4	
982	003542	000000		HLT		:SUB 16034 FROM 1 FAILED
983		000001		N=N+1&17525+N		
984		016435		M=M+401		
985	003544	000240		SCOPE		
986	003546	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
987	003552	162700	016435	SUB	#16435,%0	:SUB 16435 FROM 1
988	003556	022700	161344	CMP	#1-16435,%0	:DOES REGISTER 0=16435-1
989	003562	001401		BEQ	.+4	
990	003564	000000		HLT		:SUB 16435 FROM 1 FAILED
991		000001		N=N+1&17525+N		
992		017036		M=M+401		
993	003566	000240		SCOPE		
994	003570	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
995	003574	162700	017036	SUB	#17036,%0	:SUB 17036 FROM 1
996	003600	022700	160743	CMP	#1-17036,%0	:DOES REGISTER 0=17036-1
997	003604	001401		BEQ	.+4	
998	003606	000000		HLT		:SUB 17036 FROM 1 FAILED
999		000001		N=N+1&17525+N		
1000		017437		M=M+401		
1001	003610	000240		SCOPE		
1002	003612	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1003	003616	162700	017437	SUB	#17437,%0	:SUB 17437 FROM 1
1004	003622	022700	160342	CMP	#1-17437,%0	:DOES REGISTER 0=17437-1
1005	003626	001401		BEQ	.+4	
1006	003630	000000		HLT		:SUB 17437 FROM 1 FAILED
1007		000001		N=N+1&17525+N		
1008		020040		M=M+401		
1009	003632	000240		SCOPE		
1010	003634	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1011	003640	162700	020040	SUB	#20040,%0	:SUB 20040 FROM 1
1012	003644	022700	157741	CMP	#1-20040,%0	:DOES REGISTER 0=20040-1
1013	003650	001401		BEQ	.+4	
1014	003652	000000		HLT		:SUB 20040 FROM 1 FAILED
1015		000001		N=N+1&17525+N		
1016		020441		M=M+401		
1017	003654	000240		SCOPE		
1018	003656	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1019	003662	162700	020441	SUB	#20441,%0	:SUB 20441 FROM 1
1020	003666	022700	157340	CMP	#1-20441,%0	:DOES REGISTER 0=20441-1
1021	003672	001401		BEQ	.+4	
1022	003674	000000		HLT		:SUB 20441 FROM 1 FAILED
1023		000001		N=N+1&17525+N		
1024		021042		M=M+401		
1025	003676	000240		SCOPE		

1026	003700	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1027	003704	162700	021042	SUB	#21042,%0	:SUB 21042 FROM 1
1028	003710	022700	156737	CMP	#1-21042,%0	:DOES REGISTER 0=21042-1
1029	003714	001401		BEQ	.+4	
1030	003716	000000		HLT		:SUB 21042 FROM 1 FAILED
1031		000001		N=N+1&17525+N		
1032		021443		M=M+401		
1033	003720	000240		SCOPE		
1034	003722	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1035	003726	162700	021443	SUB	#21443,%0	:SUB 21443 FROM 1
1036	003732	022700	156336	CMP	#1-21443,%0	:DOES REGISTER 0=21443-1
1037	003736	001401		BEQ	.+4	
1038	003740	000000		HLT		:SUB 21443 FROM 1 FAILED
1039		000001		N=N+1&17525+N		
1040		022044		M=M+401		
1041	003742	000240		SCOPE		
1042	003744	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1043	003750	162700	022044	SUB	#22044,%0	:SUB 22044 FROM 1
1044	003754	022700	155735	CMP	#1-22044,%0	:DOES REGISTER 0=22044-1
1045	003760	001401		BEQ	.+4	
1046	003762	000000		HLT		:SUB 22044 FROM 1 FAILED
1047		000001		N=N+1&17525+N		
1048		022445		M=M+401		
1049	003764	000240		SCOPE		
1050	003766	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1051	003772	162700	022445	SUB	#22445,%0	:SUB 22445 FROM 1
1052	003776	022700	155334	CMP	#1-22445,%0	:DOES REGISTER 0=22445-1
1053	004002	001401		BEQ	.+4	
1054	004004	000000		HLT		:SUB 22445 FROM 1 FAILED
1055		000001		N=N+1&17525+N		
1056		023046		M=M+401		
1057	004006	000240		SCOPE		
1058	004010	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1059	004014	162700	023046	SUB	#23046,%0	:SUB 23046 FROM 1
1060	004020	022700	154733	CMP	#1-23046,%0	:DOES REGISTER 0=23046-1
1061	004024	001401		BEQ	.+4	
1062	004026	000000		HLT		:SUB 23046 FROM 1 FAILED
1063		000001		N=N+1&17525+N		
1064		023447		M=M+401		
1065	004030	000240		SCOPE		
1066	004032	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1067	004036	162700	023447	SUB	#23447,%0	:SUB 23447 FROM 1
1068	004042	022700	154332	CMP	#1-23447,%0	:DOES REGISTER 0=23447-1
1069	004046	001401		BEQ	.+4	
1070	004050	000000		HLT		:SUB 23447 FROM 1 FAILED
1071		000001		N=N+1&17525+N		
1072		024050		M=M+401		
1073	004052	000240		SCOPE		
1074	004054	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1075	004060	162700	024050	SUB	#24050,%0	:SUB 24050 FROM 1
1076	004064	022700	153731	CMP	#1-24050,%0	:DOES REGISTER 0=24050-1
1077	004070	001401		BEQ	.+4	
1078	004072	000000		HLT		:SUB 24050 FROM 1 FAILED
1079		000001		N=N+1&17525+N		
1080		024451		M=M+401		
1081	004074	000240		SCOPE		

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1082	004076	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1083	004102	162700	024451	SUB	#24451,%0	;SUB 24451 FROM 1
1084	004106	022700	153330	CMP	#1-24451,%0	;DOES REGISTER 0=24451-1
1085	004112	001401		BEQ	.+4	
1086	004114	000000		HLT		;SUB 24451 FROM 1 FAILED
1087		000001		N=N+1&17525+N		
1088		025052		M=M+401		
1089	004116	000240		SCOPE		
1090	004120	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1091	004124	162700	025052	SUB	#25052,%0	;SUB 25052 FROM 1
1092	004130	022700	152727	CMP	#1-25052,%0	;DOES REGISTER 0=25052-1
1093	004134	001401		BEQ	.+4	
1094	004136	000000		HLT		;SUB 25052 FROM 1 FAILED
1095		000001		N=N+1&17525+N		
1096		025453		M=M+401		
1097	004140	000240		SCOPE		
1098	004142	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1099	004146	162700	025453	SUB	#25453,%0	;SUB 25453 FROM 1
1100	004152	022700	152326	CMP	#1-25453,%0	;DOES REGISTER 0=25453-1
1101	004156	001401		BEQ	.+4	
1102	004160	000000		HLT		;SUB 25453 FROM 1 FAILED
1103		000001		N=N+1&17525+N		
1104		026054		M=M+401		
1105	004162	000240		SCOPE		
1106	004164	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1107	004170	162700	026054	SUB	#26054,%0	;SUB 26054 FROM 1
1108	004174	022700	151725	CMP	#1-26054,%0	;DOES REGISTER 0=26054-1
1109	004200	001401		BEQ	.+4	
1110	004202	000000		HLT		;SUB 26054 FROM 1 FAILED
1111		000001		N=N+1&17525+N		
1112		026455		M=M+401		
1113	004204	000240		SCOPE		
1114	004206	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1115	004212	162700	026455	SUB	#26455,%0	;SUB 26455 FROM 1
1116	004216	022700	151324	CMP	#1-26455,%0	;DOES REGISTER 0=26455-1
1117	004222	001401		BEQ	.+4	
1118	004224	000000		HLT		;SUB 26455 FROM 1 FAILED
1119		000001		N=N+1&17525+N		
1120		027056		M=M+401		
1121	004226	000240		SCOPE		
1122	004230	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1123	004234	162700	027056	SUB	#27056,%0	;SUB 27056 FROM 1
1124	004240	022700	150723	CMP	#1-27056,%0	;DOES REGISTER 0=27056-1
1125	004244	001401		BEQ	.+4	
1126	004246	000000		HLT		;SUB 27056 FROM 1 FAILED
1127		000001		N=N+1&17525+N		
1128		027457		M=M+401		
1129	004250	000240		SCOPE		
1130	004252	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1131	004255	162700	027457	SUB	#27457,%0	;SUB 27457 FROM 1
1132	004262	022700	150322	CMP	#1-27457,%0	;DOES REGISTER 0=27457-1
1133	004266	001401		BEQ	.+4	
1134	004270	000000		HLT		;SUB 27457 FROM 1 FAILED
1135		000001		N=N+1&17525+N		
1136		030060		M=M+401		
1137	004272	000240		SCOPE		

1138	004274	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1139	004300	162700	030060	SUB	#30060,%0	;SUB 30060 FROM 1
1140	004304	022700	147721	CMP	#1-30060,%0	;DOES REGISTER 0=30060-1
1141	004310	001401		BEQ	.+4	
1142	004312	000000		HLT		;SUB 30060 FROM 1 FAILED
1143		000001		N=N+1&17525+N		
1144		030461		M=M+401		
1145	004314	000240		SCOPE		
1146	004316	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1147	004322	162700	030461	SUB	#30461,%0	;SUB 30461 FROM 1
1148	004326	022700	147320	CMP	#1-30461,%0	;DOES REGISTER 0=30461-1
1149	004332	001401		BEQ	.+4	
1150	004334	000000		HLT		;SUB 30461 FROM 1 FAILED
1151		000001		N=N+1&17525+N		
1152		031062		M=M+401		
1153	004336	000240		SCOPE		
1154	004340	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1155	004344	162700	031062	SUB	#31062,%0	;SUB 31062 FROM 1
1156	004350	022700	146717	CMP	#1-31062,%0	;DOES REGISTER 0=31062-1
1157	004354	001401		BEQ	.+4	
1158	004356	000000		HLT		;SUB 31062 FROM 1 FAILED
1159		000001		N=N+1&17525+N		
1160		031463		M=M+401		
1161	004360	000240		SCOPE		
1162	004362	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1163	004366	162700	031463	SUB	#31463,%0	;SUB 31463 FROM 1
1164	004372	022700	146316	CMP	#1-31463,%0	;DOES REGISTER 0=31463-1
1165	004376	001401		BEQ	.+4	
1166	004400	000000		HLT		;SUB 31463 FROM 1 FAILED
1167		000001		N=N+1&17525+N		
1168		032064		M=M+401		
1169	004402	000240		SCOPE		
1170	004404	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1171	004410	162700	032064	SUB	#32064,%0	;SUB 32064 FROM 1
1172	004414	022700	145715	CMP	#1-32064,%0	;DOES REGISTER 0=32064-1
1173	004420	001401		BEQ	.+4	
1174	004422	000000		HLT		;SUB 32064 FROM 1 FAILED
1175		000001		N=N+1&17525+N		
1176		032465		M=M+401		
1177	004424	000240		SCOPE		
1178	004426	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1179	004432	162700	032465	SUB	#32465,%0	;SUB 32465 FROM 1
1180	004436	022700	145314	CMP	#1-32465,%0	;DOES REGISTER 0=32465-1
1181	004442	001401		BEQ	.+4	
1182	004444	000000		HLT		;SUB 32465 FROM 1 FAILED
1183		000001		N=N+1&17525+N		
1184		033066		M=M+401		
1185	004446	000240		SCOPE		
1186	004450	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1187	004454	162700	033066	SUB	#33066,%0	;SUB 33066 FROM 1
1188	004460	022700	144713	CMP	#1-33066,%0	;DOES REGISTER 0=33066-1
1189	004464	001401		BEQ	.+4	
1190	004466	000000		HLT		;SUB 33066 FROM 1 FAILED
1191		000001		N=N+1&17525+N		
1192		033467		M=M+401		
1193	004470	000240		SCOPE		

1194	004472	012700	000001	MOV #1,%0 ;MOV 1 TO REGISTER ZERO
1195	004476	162700	033467	SUB #33467,%0 ;SUB 33467 FROM 1
1196	004502	022700	144312	CMP #1-33467,%0 ;DOES REGISTER 0=33467-1
1197	004506	001401		BEQ .+4
1198	004510	000000		HLT ;SUB 33467 FROM 1 FAILED
1199		000001		N=N+1&17525+N
1200		034070		M=M+401
1201	004512	000240		SCOPE
1202	004514	012700	000001	MOV #1,%0 ;MOV 1 TO REGISTER ZERO
1203	004520	162700	034070	SUB #34070,%0 ;SUB 34070 FROM 1
1204	004524	022700	143711	CMP #1-34070,%0 ;DOES REGISTER 0=34070-1
1205	004530	001401		BEQ .+4
1206	004532	000000		HLT ;SUB 34070 FROM 1 FAILED
1207		000001		N=N+1&17525+N
1208		034471		M=M+401
1209	004534	000240		SCOPE
1210	004536	012700	000001	MOV #1,%0 ;MOV 1 TO REGISTER ZERO
1211	004542	162700	034471	SUB #34471,%0 ;SUB 34471 FROM 1
1212	004546	022700	143310	CMP #1-34471,%0 ;DOES REGISTER 0=34471-1
1213	004552	001401		BEQ .+4
1214	004554	000000		HLT ;SUB 34471 FROM 1 FAILED
1215		000001		N=N+1&17525+N
1216		035072		M=M+401
1217	004556	000240		SCOPE
1218	004560	012700	000001	MOV #1,%0 ;MOV 1 TO REGISTER ZERO
1219	004564	162700	035072	SUB #35072,%0 ;SUB 35072 FROM 1
1220	004570	022700	142707	CMP #1-35072,%0 ;DOES REGISTER 0=35072-1
1221	004574	001401		BEQ .+4
1222	004576	000000		HLT ;SUB 35072 FROM 1 FAILED
1223		000001		N=N+1&17525+N
1224		035473		M=M+401
1225	004600	000240		SCOPE
1226	004602	012700	000001	MOV #1,%0 ;MOV 1 TO REGISTER ZERO
1227	004606	162700	035473	SUB #35473,%0 ;SUB 35473 FROM 1
1228	004612	022700	142306	CMP #1-35473,%0 ;DOES REGISTER 0=35473-1
1229	004616	001401		BEQ .+4
1230	004620	000000		HLT ;SUB 35473 FROM 1 FAILED
1231		000001		N=N+1&17525+N
1232		036074		M=M+401
1233	004622	000240		SCOPE
1234	004624	012700	000001	MOV #1,%0 ;MOV 1 TO REGISTER ZERO
1235	004630	162700	036074	SUB #36074,%0 ;SUB 36074 FROM 1
1236	004634	022700	141705	CMP #1-36074,%0 ;DOES REGISTER 0=36074-1
1237	004640	001401		BEQ .+4
1238	004642	000000		HLT ;SUB 36074 FROM 1 FAILED
1239		000001		N=N+1&17525+N
1240		036475		M=M+401
1241	004644	000240		SCOPE
1242	004646	012700	000001	MOV #1,%0 ;MOV 1 TO REGISTER ZERO
1243	004652	162700	036475	SUB #36475,%0 ;SUB 36475 FROM 1
1244	004656	022700	141304	CMP #1-36475,%0 ;DOES REGISTER 0=36475-1
1245	004662	001401		BEQ .+4
1246	004664	000000		HLT ;SUB 36475 FROM 1 FAILED
1247		000001		N=N+1&17525+N
1248		037076		M=M+401
1249	004666	000240		SCOPE

1250	004670	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1251	004674	162700	037076	SUB	#37076,%0	;SUB 37076 FROM 1
1252	004700	022700	140703	CMP	#1-37076,%0	;DOES REGISTER 0=37076-1
1253	004704	001401		BEQ	+.4	
1254	004706	000000		HLT		;SUB 37076 FROM 1 FAILED
1255		000001		N=N+1&17525+N		
1256		037477		M=M+401		
1257	004710	000240		SCOPE		
1258	004712	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1259	004716	162700	037477	SUB	#37477,%0	;SUB 37477 FROM 1
1260	004722	022700	140302	CMP	#1-37477,%0	;DOES REGISTER 0=37477-1
1261	004726	001401		BEQ	+.4	
1262	004730	000000		HLT		;SUB 37477 FROM 1 FAILED
1263		000001		N=N+1&17525+N		
1264		040100		M=M+401		
1265	004732	000240		SCOPE		
1266	004734	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1267	004740	162700	040100	SUB	#40100,%0	;SUB 40100 FROM 1
1268	004744	022700	137701	CMP	#1-40100,%0	;DOES REGISTER 0=40100-1
1269	004750	001401		BEQ	+.4	
1270	004752	000000		HLT		;SUB 40100 FROM 1 FAILED
1271		000001		N=N+1&17525+N		
1272		040501		M=M+401		
1273	004754	000240		SCOPE		
1274	004756	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1275	004762	162700	040501	SUB	#40501,%0	;SUB 40501 FROM 1
1276	004766	022700	137300	CMP	#1-40501,%0	;DOES REGISTER 0=40501-1
1277	004772	001401		BEQ	+.4	
1278	004774	000000		HLT		;SUB 40501 FROM 1 FAILED
1279		000001		N=N+1&17525+N		
1280		041102		M=M+401		
1281	004776	000240		SCOPE		
1282	005000	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1283	005004	162700	041102	SUB	#41102,%0	;SUB 41102 FROM 1
1284	005010	022700	136677	CMP	#1-41102,%0	;DOES REGISTER 0=41102-1
1285	005014	001401		BEQ	+.4	
1286	005016	000000		HLT		;SUB 41102 FROM 1 FAILED
1287		000001		N=N+1&17525+N		
1288		041503		M=M+401		
1289	005020	000240		SCOPE		
1290	005022	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1291	005026	162700	041503	SUB	#41503,%0	;SUB 41503 FROM 1
1292	005032	022700	136276	CMP	#1-41503,%0	;DOES REGISTER 0=41503-1
1293	005036	001401		BEQ	+.4	
1294	005040	000000		HLT		;SUB 41503 FROM 1 FAILED
1295		000001		N=N+1&17525+N		
1296		042104		M=M+401		
1297	005042	000240		SCOPE		
1298	005044	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1299	005050	162700	042104	SUB	#42104,%0	;SUB 42104 FROM 1
1300	005054	022700	135675	CMP	#1-42104,%0	;DOES REGISTER 0=42104-1
1301	005060	001401		BEQ	+.4	
1302	005062	000000		HLT		;SUB 42104 FROM 1 FAILED
1303		000001		N=N+1&17525+N		
1304		042505		M=M+401		
1305	005064	000240		SCOPE		

1306	005066	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1307	005072	162700	042505	SUB	#42505,%0	:SUB 42505 FROM 1
1308	005076	022700	135274	CMP	#1-42505,%0	:DOES REGISTER 0=42505-1
1309	005102	001401		BEQ	+.4	
1310	005104	000000		HLT		:SUB 42505 FROM 1 FAILED
1311		000001		N=N+1817525+N		
1312		043106		M=M+401		
1313	005106	000240		SCOPE		
1314	005110	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1315	005114	162700	043106	SUB	#43106,%0	:SUB 43106 FROM 1
1316	005120	022700	134673	CMP	#1-43106,%0	:DOES REGISTER 0=43106-1
1317	005124	001401		BEQ	+.4	
1318	005126	000000		HLT		:SUB 43106 FROM 1 FAILED
1319		000001		N=N+1817525+N		
1320		043507		M=M+401		
1321	005130	000240		SCOPE		
1322	005132	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1323	005136	162700	043507	SUB	#43507,%0	:SUB 43507 FROM 1
1324	005142	022700	134272	CMP	#1-43507,%0	:DOES REGISTER 0=43507-1
1325	005146	001401		BEQ	+.4	
1326	005150	000000		HLT		:SUB 43507 FROM 1 FAILED
1327		000001		N=N+1817525+N		
1328		044110		M=M+401		
1329	005152	000240		SCOPE		
1330	005154	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1331	005160	162700	044110	SUB	#44110,%0	:SUB 44110 FROM 1
1332	005164	022700	133671	CMP	#1-44110,%0	:DOES REGISTER 0=44110-1
1333	005170	001401		BEQ	+.4	
1334	005172	000000		HLT		:SUB 44110 FROM 1 FAILED
1335		000001		N=N+1817525+N		
1336		044511		M=M+401		
1337	005174	000240		SCOPE		
1338	005176	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1339	005202	162700	044511	SUB	#44511,%0	:SUB 44511 FROM 1
1340	005206	022700	133270	CMP	#1-44511,%0	:DOES REGISTER 0=44511-1
1341	005212	001401		BEQ	+.4	
1342	005214	000000		HLT		:SUB 44511 FROM 1 FAILED
1343		000001		N=N+1817525+N		
1344		045112		M=M+401		
1345	005216	000240		SCOPE		
1346	005220	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1347	005224	162700	045112	SUB	#45112,%0	:SUB 45112 FROM 1
1348	005230	022700	132667	CMP	#1-45112,%0	:DOES REGISTER 0=45112-1
1349	005234	001401		BEQ	+.4	
1350	005236	000000		HLT		:SUB 45112 FROM 1 FAILED
1351		000001		N=N+1817525+N		
1352		045513		M=M+401		
1353	005240	000240		SCOPE		
1354	005242	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1355	005246	162700	045513	SUB	#45513,%0	:SUB 45513 FROM 1
1356	005252	022700	132266	CMP	#1-45513,%0	:DOES REGISTER 0=45513-1
1357	005256	001401		BEQ	+.4	
1358	005260	000000		HLT		:SUB 45513 FROM 1 FAILED
1359		000001		N=N+1817525+N		
1360		046114		M=M+401		
1361	005262	000240		SCOPE		

1362	005264	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1363	005270	162700	046114	SUB	#46114,%0	:SUB 46114 FROM 1
1364	005274	022700	131665	CMP	#1-46114,%0	:DOES REGISTER 0=46114-1
1365	005300	001401		BEQ	.+4	
1366	005302	000000		HLT		:SUB 46114 FROM 1 FAILED
1367		000001		N=N+1&17525+N		
1368		046515		M=M+401		
1369	005304	000240		SCOPE		
1370	005306	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1371	005312	162700	046515	SUB	#46515,%0	:SUB 46515 FROM 1
1372	005316	022700	131264	CMP	#1-46515,%0	:DOES REGISTER 0=46515-1
1373	005322	001401		BEQ	.+4	
1374	005324	000000		HLT		:SUB 46515 FROM 1 FAILED
1375		000001		N=N+1&17525+N		
1376		047116		M=M+401		
1377	005326	000240		SCOPE		
1378	005330	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1379	005334	162700	047116	SUB	#47116,%0	:SUB 47116 FROM 1
1380	005340	022700	130663	CMP	#1-47116,%0	:DOES REGISTER 0=47116-1
1381	005344	001401		BEQ	.+4	
1382	005346	000000		HLT		:SUB 47116 FROM 1 FAILED
1383		000001		N=N+1&17525+N		
1384		047517		M=M+401		
1385	005350	000240		SCOPE		
1386	005352	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1387	005356	162700	047517	SUB	#47517,%0	:SUB 47517 FROM 1
1388	005362	022700	130262	CMP	#1-47517,%0	:DOES REGISTER 0=47517-1
1389	005366	001401		BEQ	.+4	
1390	005370	000000		HLT		:SUB 47517 FROM 1 FAILED
1391		000001		N=N+1&17525+N		
1392		050120		M=M+401		
1393	005372	000240		SCOPE		
1394	005374	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1395	005400	162700	050120	SUB	#50120,%0	:SUB 50120 FROM 1
1396	005404	022700	127661	CMP	#1-50120,%0	:DOES REGISTER 0=50120-1
1397	005410	001401		BEQ	.+4	
1398	005412	000000		HLT		:SUB 50120 FROM 1 FAILED
1399		000001		N=N+1&17525+N		
1400		050521		M=M+401		
1401	005414	000240		SCOPE		
1402	005416	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1403	005422	162700	050521	SUB	#50521,%0	:SUB 50521 FROM 1
1404	005426	022700	127260	CMP	#1-50521,%0	:DOES REGISTER 0=50521-1
1405	005432	001401		BEQ	.+4	
1406	005434	000000		HLT		:SUB 50521 FROM 1 FAILED
1407		000001		N=N+1&17525+N		
1408		051122		M=M+401		
1409	005436	000240		SCOPE		
1410	005440	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1411	005444	162700	051122	SUB	#51122,%0	:SUB 51122 FROM 1
1412	005450	022700	126657	CMP	#1-51122,%0	:DOES REGISTER 0=51122-1
1413	005454	001401		BEQ	.+4	
1414	005456	000000		HLT		:SUB 51122 FROM 1 FAILED
1415		000001		N=N+1&17525+N		
1416		051523		M=M+401		
1417	005460	000240		SCOPE		

1418	005462	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1419	005466	162700	051523	SUB	#51523,%0	:SUB 51523 FROM 1
1420	005472	022700	126256	CMP	#1-51523,%0	:DOES REGISTER 0=51523-1
1421	005476	001401		BEQ	+.4	
1422	005500	000000		HLT		:SUB 51523 FROM 1 FAILED
1423		000001		N=N+1&17525+N		
1424		052124		M=M+401		
1425	005502	000240		SCOPE		
1426	005504	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1427	005510	162700	052124	SUB	#52124,%0	:SUB 52124 FROM 1
1428	005514	022700	125655	CMP	#1-52124,%0	:DOES REGISTER 0=52124-1
1429	005520	001401		BEQ	+.4	
1430	005522	000000		HLT		:SUB 52124 FROM 1 FAILED
1431		000001		N=N+1&17525+N		
1432		052525		M=M+401		
1433	005524	000240		SCOPE		
1434	005526	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1435	005532	162700	052525	SUB	#52525,%0	:SUB 52525 FROM 1
1436	005536	022700	125254	CMP	#1-52525,%0	:DOES REGISTER 0=52525-1
1437	005542	001401		BEQ	+.4	
1438	005544	000000		HLT		:SUB 52525 FROM 1 FAILED
1439		000001		N=N+1&17525+N		
1440		053126		M=M+401		
1441	005546	000240		SCOPE		
1442	005550	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1443	005554	162700	053126	SUB	#53126,%0	:SUB 53126 FROM 1
1444	005560	022700	124653	CMP	#1-53126,%0	:DOES REGISTER 0=53126-1
1445	005564	001401		BEQ	+.4	
1446	005566	000000		HLT		:SUB 53126 FROM 1 FAILED
1447		000001		N=N+1&17525+N		
1448		053527		M=M+401		
1449	005570	000240		SCOPE		
1450	005572	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1451	005576	162700	053527	SUB	#53527,%0	:SUB 53527 FROM 1
1452	005602	022700	124252	CMP	#1-53527,%0	:DOES REGISTER 0=53527-1
1453	005606	001401		BEQ	+.4	
1454	005610	000000		HLT		:SUB 53527 FROM 1 FAILED
1455		000001		N=N+1&17525+N		
1456		054130		M=M+401		
1457	005612	000240		SCOPE		
1458	005614	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1459	005620	162700	054130	SUB	#54130,%0	:SUB 54130 FROM 1
1460	005624	022700	123651	CMP	#1-54130,%0	:DOES REGISTER 0=54130-1
1461	005630	001401		BEQ	+.4	
1462	005632	000000		HLT		:SUB 54130 FROM 1 FAILED
1463		000001		N=N+1&17525+N		
1464		054531		M=M+401		
1465	005634	000240		SCOPE		
1466	005636	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1467	005642	162700	054531	SUB	#54531,%0	:SUB 54531 FROM 1
1468	005646	022700	123250	CMP	#1-54531,%0	:DOES REGISTER 0=54531-1
1469	005652	001401		BEQ	+.4	
1470	005654	000000		HLT		:SUB 54531 FROM 1 FAILED
1471		000001		N=N+1&17525+N		
1472		055132		M=M+401		
1473	005656	000240		SCOPE		

1474	005660	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1475	005664	162700	055132	SUB	#55132,%0	:SUB 55132 FROM 1
1476	005670	022700	122647	CMP	#1-55132,%0	:DOES REGISTER 0=55132-1
1477	005674	001401		BEQ	+.4	
1478	005676	000000		HLT		:SUB 55132 FROM 1 FAILED
1479		000001		N=N+1&17525+N		
1480		055533		M=M+401		
1481	005700	000240		SCOPE		
1482	005702	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1483	005706	162700	055533	SUB	#55533,%0	:SUB 55533 FROM 1
1484	005712	022700	122246	CMP	#1-55533,%0	:DOES REGISTER 0=55533-1
1485	005716	001401		BEQ	+.4	
1486	005720	000000		HLT		:SUB 55533 FROM 1 FAILED
1487		000001		N=N+1&17525+N		
1488		056134		M=M+401		
1489	005722	000240		SCOPE		
1490	005724	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1491	005730	162700	056134	SUB	#56134,%0	:SUB 56134 FROM 1
1492	005734	022700	121645	CMP	#1-56134,%0	:DOES REGISTER 0=56134-1
1493	005740	001401		BEQ	+.4	
1494	005742	000000		HLT		:SUB 56134 FROM 1 FAILED
1495		000001		N=N+1&17525+N		
1496		056535		M=M+401		
1497	005744	000240		SCOPE		
1498	005746	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1499	005752	162700	056535	SUB	#56535,%0	:SUB 56535 FROM 1
1500	005756	022700	121244	CMP	#1-56535,%0	:DOES REGISTER 0=56535-1
1501	005762	001401		BEQ	+.4	
1502	005764	000000		HLT		:SUB 56535 FROM 1 FAILED
1503		000001		N=N+1&17525+N		
1504		057136		M=M+401		
1505	005766	000240		SCOPE		
1506	005770	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1507	005774	162700	057136	SUB	#57136,%0	:SUB 57136 FROM 1
1508	006000	022700	120643	CMP	#1-57136,%0	:DOES REGISTER 0=57136-1
1509	006004	001401		BEQ	+.4	
1510	006006	000000		HLT		:SUB 57136 FROM 1 FAILED
1511		000001		N=N+1&17525+N		
1512		057537		M=M+401		
1513	006010	000240		SCOPE		
1514	006012	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1515	006016	162700	057537	SUB	#57537,%0	:SUB 57537 FROM 1
1516	006022	022700	120242	CMP	#1-57537,%0	:DOES REGISTER 0=57537-1
1517	006026	001401		BEQ	+.4	
1518	006030	000000		HLT		:SUB 57537 FROM 1 FAILED
1519		000001		N=N+1&17525+N		
1520		060140		M=M+401		
1521	006032	000240		SCOPE		
1522	006034	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1523	006040	162700	060140	SUB	#60140,%0	:SUB 60140 FROM 1
1524	006044	022700	117641	CMP	#1-60140,%0	:DOES REGISTER 0=60140-1
1525	006050	001401		BEQ	+.4	
1526	006052	000000		HLT		:SUB 60140 FROM 1 FAILED
1527		000001		N=N+1&17525+N		
1528		060541		M=M+401		
1529	006054	000240		SCOPE		

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1530	006056	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1531	006062	162700	060541	SUB	#60541,%0	:SUB 60541 FROM 1
1532	006066	022700	117240	CMP	#1-60541,%0	:DOES REGISTER 0=60541-1
1533	006072	001401		BEQ	.+4	
1534	006074	000000		HLT		:SUB 60541 FROM 1 FAILED
1535		000001		N=N+1&17525+N		
1536		061142		M=M+401		
1537	006076	000240		SCOPE		
1538	006100	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1539	006104	162700	061142	SUB	#61142,%0	:SUB 61142 FROM 1
1540	006110	022700	116637	CMP	#1-61142,%0	:DOES REGISTER 0=61142-1
1541	006114	001401		BEQ	.+4	
1542	006116	000000		HLT		:SUB 61142 FROM 1 FAILED
1543		000001		N=N+1&17525+N		
1544		061543		M=M+401		
1545	006120	000240		SCOPE		
1546	006122	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1547	006126	162700	061543	SUB	#61543,%0	:SUB 61543 FROM 1
1548	006132	022700	116236	CMP	#1-61543,%0	:DOES REGISTER 0=61543-1
1549	006136	001401		BEQ	.+4	
1550	006140	000000		HLT		:SUB 61543 FROM 1 FAILED
1551		000001		N=N+1&17525+N		
1552		062144		M=M+401		
1553	006142	000240		SCOPE		
1554	006144	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1555	006150	162700	062144	SUB	#62144,%0	:SUB 62144 FROM 1
1556	006154	022700	115635	CMP	#1-62144,%0	:DOES REGISTER 0=62144-1
1557	006160	001401		BEQ	.+4	
1558	006162	000000		HLT		:SUB 62144 FROM 1 FAILED
1559		000001		N=N+1&17525+N		
1560		062545		M=M+401		
1561	006164	000240		SCOPE		
1562	006166	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1563	006172	162700	062545	SUB	#62545,%0	:SUB 62545 FROM 1
1564	006176	022700	115234	CMP	#1-62545,%0	:DOES REGISTER 0=62545-1
1565	006202	001401		BEQ	.+4	
1566	006204	000000		HLT		:SUB 62545 FROM 1 FAILED
1567		000001		N=N+1&17525+N		
1568		063146		M=M+401		
1569	006206	000240		SCOPE		
1570	006210	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1571	006214	162700	063146	SUB	#63146,%0	:SUB 63146 FROM 1
1572	006220	022700	114633	CMP	#1-63146,%0	:DOES REGISTER 0=63146-1
1573	006224	001401		BEQ	.+4	
1574	006226	000000		HLT		:SUB 63146 FROM 1 FAILED
1575		000001		N=N+1&17525+N		
1576		063547		M=M+401		
1577	006230	000240		SCOPE		
1578	006232	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1579	006236	162700	063547	SUB	#63547,%0	:SUB 63547 FROM 1
1580	006242	022700	114232	CMP	#1-63547,%0	:DOES REGISTER 0=63547-1
1581	006246	001401		BEQ	.+4	
1582	006250	000000		HLT		:SUB 63547 FROM 1 FAILED
1583		000001		N=N+1&17525+N		
1584		064150		M=M+401		
1585	006252	000240		SCOPE		

1586	006254	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
1587	006260	162700	064150	SUB	#64150,%0	;SUB 64150 FROM 1
1588	006264	022700	113631	CMP	#1-64150,%0	;DOES REGISTER 0=64150-1
1589	006270	001401		BEQ	.+4	
1590	006272	000000		HLT		;SUB 64150 FROM 1 FAILED
1591		000001		N=N+1&17525+N		
1592		064551		M=M+401		
1593	006274	000240		SCOPE		
1594	006276	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
1595	006302	162700	064551	SUB	#64551,%0	;SUB 64551 FROM 1
1596	006306	022700	113230	CMP	#1-64551,%0	;DOES REGISTER 0=64551-1
1597	006312	001401		BEQ	.+4	
1598	006314	000000		HLT		;SUB 64551 FROM 1 FAILED
1599		000001		N=N+1&17525+N		
1600		065152		M=M+401		
1601	006316	000240		SCOPE		
1602	006320	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
1603	006324	162700	065152	SUB	#65152,%0	;SUB 65152 FROM 1
1604	006330	022700	112627	CMP	#1-65152,%0	;DOES REGISTER 0=65152-1
1605	006334	001401		BEQ	.+4	
1606	006336	000000		HLT		;SUB 65152 FROM 1 FAILED
1607		000001		N=N+1&17525+N		
1608		065553		M=M+401		
1609	006340	000240		SCOPE		
1610	006342	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
1611	006346	162700	065553	SUB	#65553,%0	;SUB 65553 FROM 1
1612	006352	022700	112226	CMP	#1-65553,%0	;DOES REGISTER 0=65553-1
1613	006356	001401		BEQ	.+4	
1614	006360	000000		HLT		;SUB 65553 FROM 1 FAILED
1615		000001		N=N+1&17525+N		
1616		066154		M=M+401		
1617	006362	000240		SCOPE		
1618	006364	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
1619	006370	162700	066154	SUB	#66154,%0	;SUB 66154 FROM 1
1620	006374	022700	111625	CMP	#1-66154,%0	;DOES REGISTER 0=66154-1
1621	006400	001401		BEQ	.+4	
1622	006402	000000		HLT		;SUB 66154 FROM 1 FAILED
1623		000001		N=N+1&17525+N		
1624		066555		M=M+401		
1625	006404	000240		SCOPE		
1626	006406	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
1627	006412	162700	066555	SUB	#66555,%0	;SUB 66555 FROM 1
1628	006416	022700	111224	CMP	#1-66555,%0	;DOES REGISTER 0=66555-1
1629	006422	001401		BEQ	.+4	
1630	006424	000000		HLT		;SUB 66555 FROM 1 FAILED
1631		000001		N=N+1&17525+N		
1632		067156		M=M+401		
1633	006426	000240		SCOPE		
1634	006430	012700	000001	MOV	#1,%0	;MOV 1 TO REGISTER ZERO
1635	006434	162700	067156	SUB	#67156,%0	;SUB 67156 FROM 1
1636	006440	022700	110623	CMP	#1-67156,%0	;DOES REGISTER 0=67156-1
1637	006444	001401		BEQ	.+4	
1638	006446	000000		HLT		;SUB 67156 FROM 1 FAILED
1639		000001		N=N+1&17525+N		
1640		067557		M=M+401		
1641	006450	000240		SCOPE		

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1642	006452	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1643	006456	162700	067557	SUB	#67557,%0	:SUB 67557 FROM 1
1644	006462	022700	110222	CMP	#1-67557,%0	:DOES REGISTER 0=67557-1
1645	006466	001401		BEQ	.+4	
1646	006470	000000		HLT		:SUB 67557 FROM 1 FAILED
1647		000001		N=N+1&17525+N		
1648		070160		M=M+401		
1649	006472	000240		SCOPE		
1650	006474	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1651	006500	162700	070160	SUB	#70160,%0	:SUB 70160 FROM 1
1652	006504	022700	107621	CMP	#1-70160,%0	:DOES REGISTER 0=70160-1
1653	006510	001401		BEQ	.+4	
1654	006512	000000		HLT		:SUB 70160 FROM 1 FAILED
1655		000001		N=N+1&17525+N		
1656		070561		M=M+401		
1657	006514	000240		SCOPE		
1658	006516	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1659	006522	162700	070561	SUB	#70561,%0	:SUB 70561 FROM 1
1660	006526	022700	107220	CMP	#1-70561,%0	:DOES REGISTER 0=70561-1
1661	006532	001401		BEQ	.+4	
1662	006534	000000		HLT		:SUB 70561 FROM 1 FAILED
1663		000001		N=N+1&17525+N		
1664		071162		M=M+401		
1665	006536	000240		SCOPE		
1666	006540	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1667	006544	162700	071162	SUB	#71162,%0	:SUB 71162 FROM 1
1668	006550	022700	106617	CMP	#1-71162,%0	:DOES REGISTER 0=71162-1
1669	006554	001401		BEQ	.+4	
1670	006556	000000		HLT		:SUB 71162 FROM 1 FAILED
1671		000001		N=N+1&17525+N		
1672		071563		M=M+401		
1673	006560	000240		SCOPE		
1674	006562	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1675	006566	162700	071563	SUB	#71563,%0	:SUB 71563 FROM 1
1676	006572	022700	106216	CMP	#1-71563,%0	:DOES REGISTER 0=71563-1
1677	006576	001401		BEQ	.+4	
1678	006600	000000		HLT		:SUB 71563 FROM 1 FAILED
1679		000001		N=N+1&17525+N		
1680		072164		M=M+401		
1681	006602	000240		SCOPE		
1682	006604	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1683	006610	162700	072164	SUB	#72164,%0	:SUB 72164 FROM 1
1684	006614	022700	105615	CMP	#1-72164,%0	:DOES REGISTER 0=72164-1
1685	006620	001401		BEQ	.+4	
1686	006622	000000		HLT		:SUB 72164 FROM 1 FAILED
1687		000001		N=N+1&17525+N		
1688		072565		M=M+401		
1689	006624	000240		SCOPE		
1690	006626	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1691	006632	162700	072565	SUB	#72565,%0	:SUB 72565 FROM 1
1692	006636	022700	105214	CMP	#1-72565,%0	:DOES REGISTER 0=72565-1
1693	006642	001401		BEQ	.+4	
1694	006644	000000		HLT		:SUB 72565 FROM 1 FAILED
1695		000001		N=N+1&17525+N		
1696		073166		M=M+401		
1697	006646	000240		SCOPE		

1698	006650	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1699	006654	162700	073166	SUB	#73166,%0	:SUB 73166 FROM 1
1700	006660	022700	104613	CMP	#1-73166,%0	:DOES REGISTER 0=73166-1
1701	006664	001401		BEQ	.+4	
1702	006666	000000		HLT		:SUB 73166 FROM 1 FAILED
1703		000001		N=N+1&17525+N		
1704		073567		M=M+401		
1705	006670	000240		SCOPE		
1706	006672	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1707	006676	162700	073567	SUB	#73567,%0	:SUB 73567 FROM 1
1708	006702	022700	104212	CMP	#1-73567,%0	:DOES REGISTER 0=73567-1
1709	006706	001401		BEQ	.+4	
1710	006710	000000		HLT		:SUB 73567 FROM 1 FAILED
1711		000001		N=N+1&17525+N		
1712		074170		M=M+401		
1713	006712	000240		SCOPE		
1714	006714	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1715	006720	162700	074170	SUB	#74170,%0	:SUB 74170 FROM 1
1716	006724	022700	103611	CMP	#1-74170,%0	:DOES REGISTER 0=74170-1
1717	006730	001401		BEQ	.+4	
1718	006732	000000		HLT		:SUB 74170 FROM 1 FAILED
1719		000001		N=N+1&17525+N		
1720		074571		M=M+401		
1721	006734	000240		SCOPE		
1722	006736	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1723	006742	162700	074571	SUB	#74571,%0	:SUB 74571 FROM 1
1724	006746	022700	103210	CMP	#1-74571,%0	:DOES REGISTER 0=74571-1
1725	006752	001401		BEQ	.+4	
1726	006754	000000		HLT		:SUB 74571 FROM 1 FAILED
1727		000001		N=N+1&17525+N		
1728		075172		M=M+401		
1729	006756	000240		SCOPE		
1730	006760	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1731	006764	162700	075172	SUB	#75172,%0	:SUB 75172 FROM 1
1732	006770	022700	102607	CMP	#1-75172,%0	:DOES REGISTER 0=75172-1
1733	006774	001401		BEQ	.+4	
1734	006776	000000		HLT		:SUB 75172 FROM 1 FAILED
1735		000001		N=N+1&17525+N		
1736		075573		M=M+401		
1737	007000	000240		SCOPE		
1738	007002	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1739	007006	162700	075573	SUB	#75573,%0	:SUB 75573 FROM 1
1740	007012	022700	102206	CMP	#1-75573,%0	:DOES REGISTER 0=75573-1
1741	007016	001401		BEQ	.+4	
1742	007020	000000		HLT		:SUB 75573 FROM 1 FAILED
1743		000001		N=N+1&17525+N		
1744		076174		M=M+401		
1745	007022	000240		SCOPE		
1746	007024	012700	000001	MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1747	007030	162700	076174	SUB	#76174,%0	:SUB 76174 FROM 1
1748	007034	022700	101605	CMP	#1-76174,%0	:DOES REGISTER 0=76174-1
1749	007040	001401		BEQ	.+4	
1750	007042	000000		HLT		:SUB 76174 FROM 1 FAILED
1751		000001		N=N+1&17525+N		
1752		076575		M=M+401		
1753	007044	000240		SCOPE		

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1754	007046	012700	000001				MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1755	007052	162700	076575				SUB	#76575,%0	:SUB 76575 FROM 1
1756	007056	022700	101204				CMP	#1-76575,%0	:DOES REGISTER 0=76575-1
1757	007062	001401					BEQ	+.4	
1758	007064	000000					HLT		:SUB 76575 FROM 1 FAILED
1759		000001						N=N+1&17525+N	
1760		077176						M=M+401	
1761	007066	000240						SCOPE	
1762	007070	012700	000001				MOV	#1,%0	:MOV 1 TO REGISTER ZERO
1763	007074	162700	077176				SUB	#77176,%0	:SUB 77176 FROM 1
1764	007100	022700	100603				CMP	#1-77176,%0	:DOES REGISTER 0=77176-1
1765	007104	001401					BEQ	+.4	
1766	007106	000000					HLT		:SUB 77176 FROM 1 FAILED
1767		000001						N=N+1&17525+N	
1768		077577						M=M+401	
1769	007110	005267	000026				INC	LOOP	
1770	007114	100010					BPL	FIN	
1771	007116	005067	000020				CLR	LOOP	
1772	007122	012737	000207	177566			MOV	#207,@#177566	
1773	007130	105737	177564				TSTB	@#177564	
1774	007134	100375					BPL	-.4	
1775	007136	000167	171036			FIN:	JMP	200	
1776	007142	000000				LOOP:	0		
1777		000001					.END		

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.MAIN. MACY11 27(732) 01-OCT-76 10:43 PAGE 38
 DOKA.P11 CROSS REFERENCE TABLE -- USER SYMBOLS

FIN = 007136
 HLT = 000000

1770	1775#												
258#	269	275	281	287	293	301	308	315	322	329	336	343	
350	357	364	371	378	385	392	399	406	415	422	429	436	
443	450	457	464	471	478	485	492	499	506	513	520	529	
536	543	550	557	564	571	578	585	592	599	606	613	620	
627	634	643	650	657	664	671	678	685	692	699	706	713	
720	727	734	741	748	758	766	774	782	790	799	806	814	
822	830	838	846	854	862	870	878	886	894	902	910	918	
926	934	942	950	958	966	974	982	990	998	1006	1014	1022	
1030	1038	1046	1054	1062	1070	1078	1086	1094	1102	1110	1118	1126	
1134	1142	1150	1158	1166	1174	1182	1190	1198	1206	1214	1222	1230	
1238	1246	1254	1262	1270	1278	1286	1294	1302	1310	1318	1326	1334	
1342	1350	1358	1366	1374	1382	1390	1398	1406	1414	1422	1430	1438	
1446	1454	1462	1470	1478	1486	1494	1502	1510	1518	1526	1534	1542	
1550	1558	1566	1574	1582	1590	1598	1606	1614	1622	1630	1638	1646	
1654	1662	1670	1678	1686	1694	1702	1710	1718	1726	1734	1742	1750	
1758	1766												

LOOP = 007142
 M = 077577

1769*	1771*	1776#											
751#	753	760#	761	768#	769	776#	777	784#	785	792#	793	800#	
801	808#	809	816#	817	824#	825	832#	833	840#	841	848#	849	
856#	857	864#	865	872#	873	880#	881	888#	889	896#	897	904#	
905	912#	913	920#	921	928#	929	936#	937	944#	945	952#	953	
960#	961	968#	969	976#	977	984#	985	992#	993	1000#	1001	1008#	
1009	1016#	1017	1024#	1025	1032#	1033	1040#	1041	1048#	1049	1056#	1057	
1064#	1065	1072#	1073	1080#	1081	1088#	1089	1096#	1097	1104#	1105	1112#	
1113	1120#	1121	1128#	1129	1136#	1137	1144#	1145	1152#	1153	1160#	1161	
1168#	1169	1176#	1177	1184#	1185	1192#	1193	1200#	1201	1208#	1209	1216#	
1217	1224#	1225	1232#	1233	1240#	1241	1248#	1249	1256#	1257	1264#	1265	
1272#	1273	1280#	1281	1288#	1289	1296#	1297	1304#	1305	1312#	1313	1320#	
1321	1328#	1329	1336#	1337	1344#	1345	1352#	1353	1360#	1361	1368#	1369	
1376#	1377	1384#	1385	1392#	1393	1400#	1401	1408#	1409	1416#	1417	1424#	
1425	1432#	1433	1440#	1441	1448#	1449	1456#	1457	1464#	1465	1472#	1473	
1480#	1481	1488#	1489	1496#	1497	1504#	1505	1512#	1513	1520#	1521	1528#	
1529	1536#	1537	1544#	1545	1552#	1553	1560#	1561	1568#	1569	1576#	1577	
1584#	1585	1592#	1593	1600#	1601	1608#	1609	1616#	1617	1624#	1625	1632#	
1627	1640#	1641	1648#	1649	1656#	1657	1664#	1665	1672#	1673	1680#	1681	
1688#	1689	1696#	1697	1704#	1705	1712#	1713	1720#	1721	1728#	1729	1736#	
1737	1744#	1745	1752#	1753	1760#	1761	1768#						

N = 000001

295#	296	302#	303	309#	310	316#	317	323#	324	330#	331	337#	
338	344#	345	351#	352	358#	359	365#	366	372#	373	379#	380	
386#	387	393#	394	400#	401	407#	409	410	416#	417	423#	424	
430#	431	437#	438	444#	445	451#	452	458#	459	465#	466	472#	
473	479#	480	486#	487	493#	494	500#	501	507#	508	514#	515	
521#	523#	524	530#	531	537#	538	544#	545	551#	552	558#	559	
565#	566	572#	573	579#	580	586#	587	593#	594	600#	601	607#	
608	614#	615	621#	622	628#	629	635#	637#	638	644#	645	651#	
652	658#	659	665#	666	672#	673	679#	680	686#	687	693#	694	
700#	701	707#	708	714#	715	721#	722	728#	729	735#	736	742#	
743	749#	752#	753	759#	761	767#	769	775#	777	783#	785	791#	
793	799#	801	807#	809	815#	817	823#	825	831#	833	839#	841	
847#	849	855#	857	863#	865	871#	873	879#	881	887#	889	895#	
897	903#	905	911#	913	919#	921	927#	929	935#	937	943#	945	
951#	953	959#	961	967#	969	975#	977	983#	985	991#	993	999#	
1001	1007#	1009	1015#	1017	1023#	1025	1031#	1033	1039#	1041	1047#	1049	
1055#	1057	1063#	1065	1071#	1073	1079#	1081	1087#	1089	1095#	1097	1103#	
1105	1111#	1113	1119#	1121	1127#	1129	1135#	1137	1143#	1145	1151#	1153	

.SERRO	1#
.SERRT	1#
.SMULT	1#
.SPOWE	1#
.SRAND	1#
.SRDDE	1#
.SRDOC	1#
.SREAD	1#
.SR2AZ	1#
.SSAVE	1#
.SSB2D	1#
.SSB2O	1#
.SSCOP	1#
.SSIZE	1#
.SSUPR	1#
.STRAP	1#
.STYPB	1#
.STYPD	1#
.STYPE	1#
.STYPO	1#
.S4DCA	1#
.117D	1#

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 DOKA.P11 CROSS REFERENCE TABLE -- PERMANENT SYMBOLS

	1283	1291	1299	1307	1315	1323	1331	1339	1347	1355	1363	1371	1379	1387	1395
	1403	1411	1419	1427	1435	1443	1451	1459	1467	1475	1483	1491	1499	1507	1515
	1523	1531	1539	1547	1555	1563	1571	1579	1587	1595	1603	1611	1619	1627	1635
	1643	1651	1659	1667	1675	1683	1691	1699	1707	1715	1723	1731	1739	1747	1755
	1763														
TSTB	1773														
.ENABL	1														
.END	1777														
.LIST	1														
.MACR	262														
.MACRO	1														
.NLIST	1														
.REM	1														
.REFT	296	410	524	638	753										

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

*.DOKA.SEG/SOL/CRF/PAGNUM/NL:TOC=SYSMAC.CO,DOKA.P11
 RUN-TIME: 25 30 2 SECONDS
 RUN-TIME RATIO: 258/58=4.3
 CORE USED: 32K (63 PAGES)

